

**REMARKS**

Claims 1-13 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the following remarks.

Turning now to the art rejections, Claims 1-6, 9, 11-13 were rejected under 35 U.S.C. § 102(b) as being anticipated by Szepesi; and Claims 7, 8, and 10 were rejected under 35 U.S.C. § 103 as being unpatentable over Szepesi.

It is respectfully submitted that Szepesi does not disclose or suggest the presently claimed invention including the capacitor directly coupled across the drains of the first FET and the second FET.

Applicants agree with the Examiner as evidence on page 2 of the Office Action that Szepesi discloses that the alleged first FET (213) and a parallel second FET (202) and the comparator (218) have diodes D1 and D2 and a low pass filter IN1 and IN2 between them.

The Examiner alleges that these devices do not alter the output.

Applicants respectfully traverse this allegation.

Diodes and low pass filters certainly alter the input signal and consequently the output signal is altered. If the signals were not altered why would the circuit designer use them?

This is not a direct connection, and consequently this reference does not anticipate or render obviousness the claimed invention.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant amendment places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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